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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/265,346 03/09/1999 ABN

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 58188/1998 03/10/1998

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 03/24/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 19	TOTAL CLAIMS 4	INDEPENDENT CLAIMS 3
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## TITLE

Method of generating test pattern for integrated circuit

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